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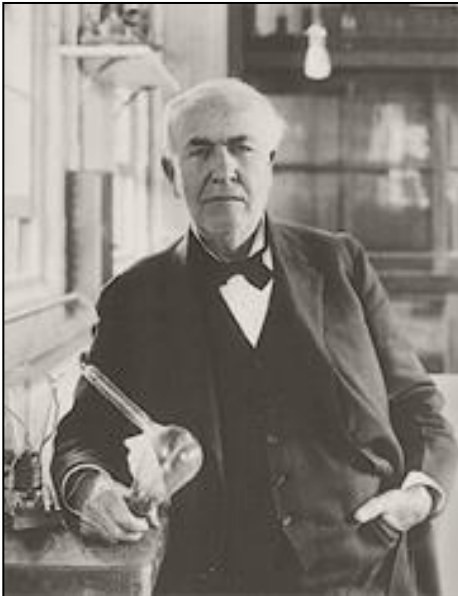
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Alexander Graham Bell

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Titanic

1963



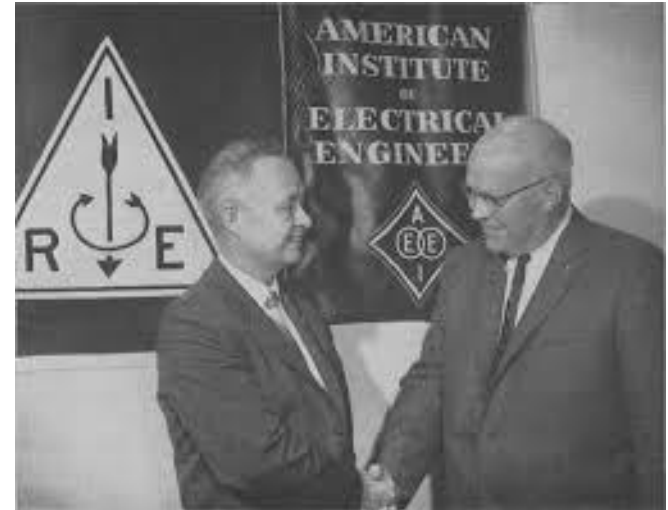
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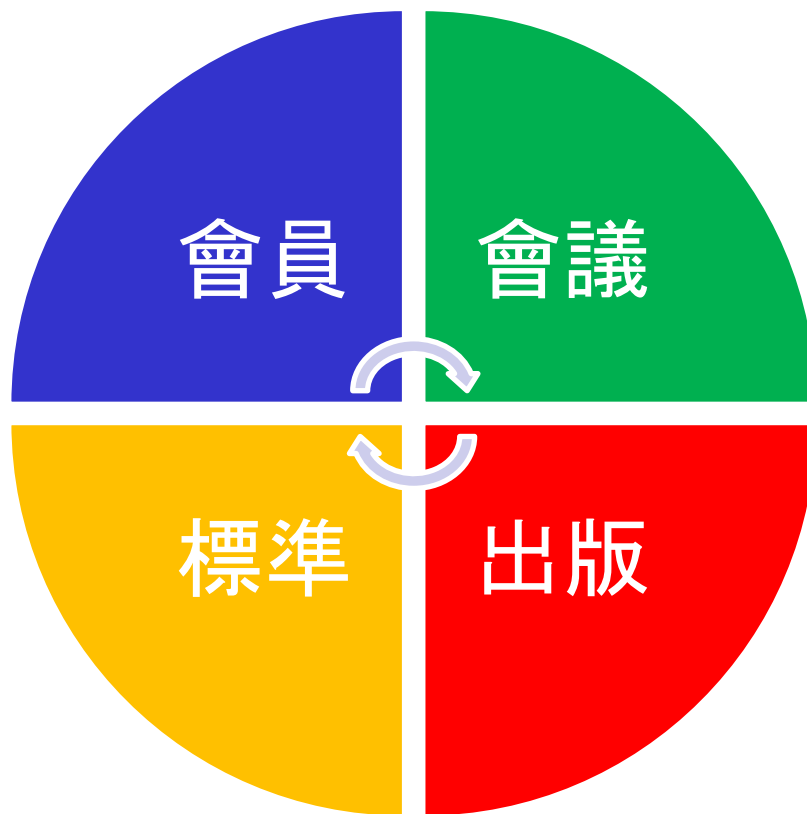


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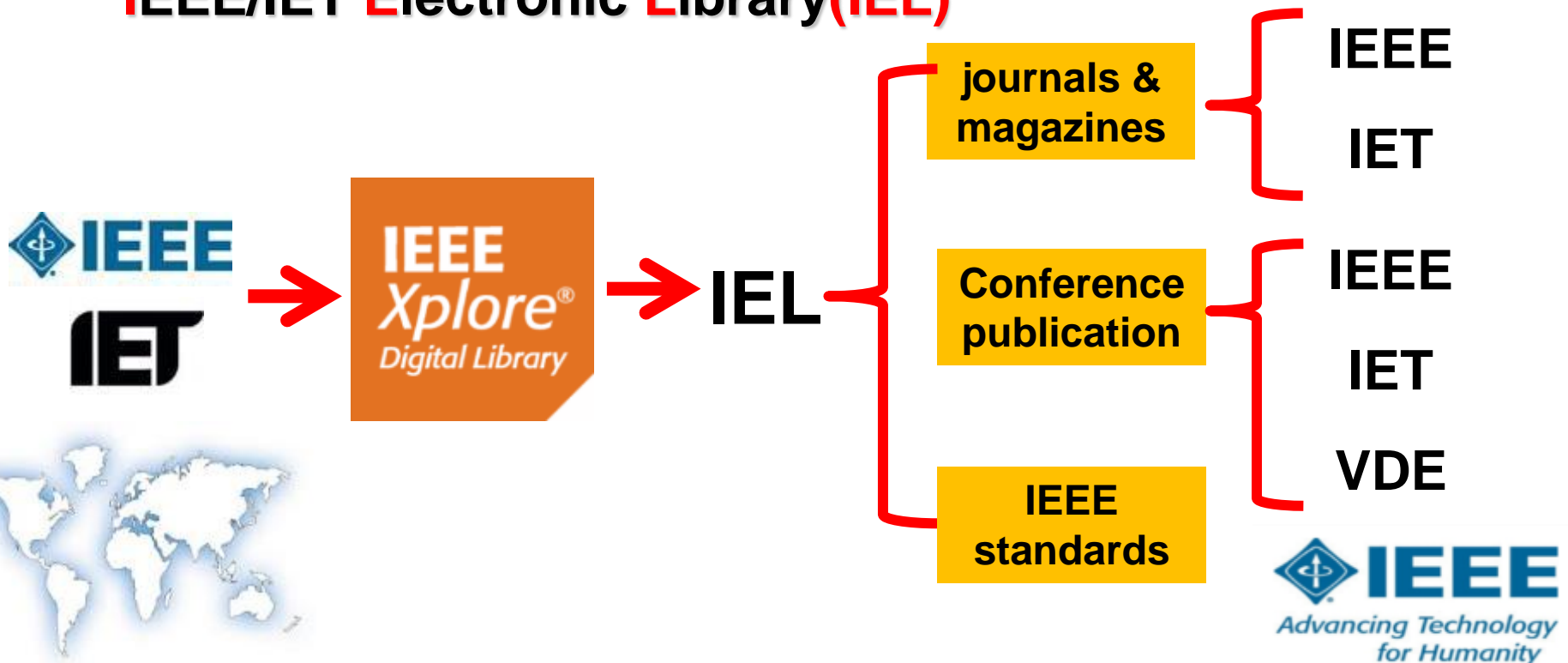
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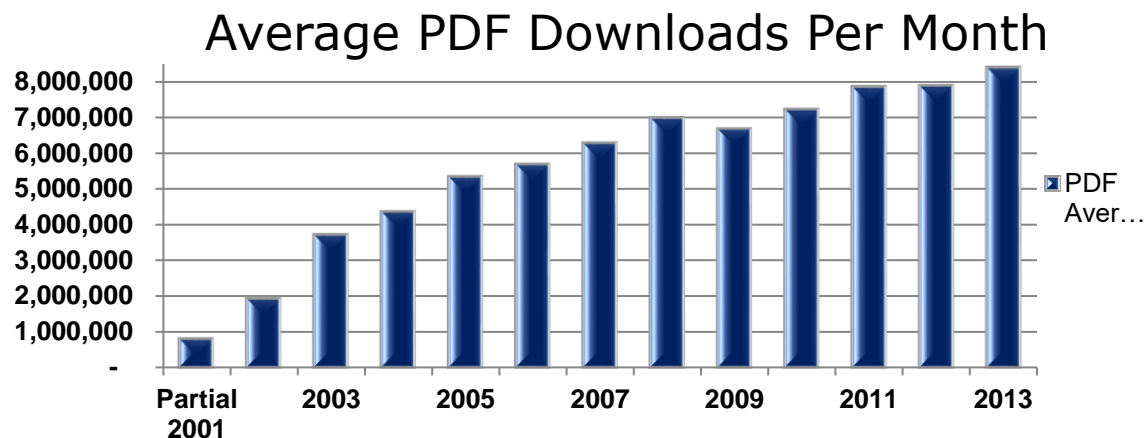
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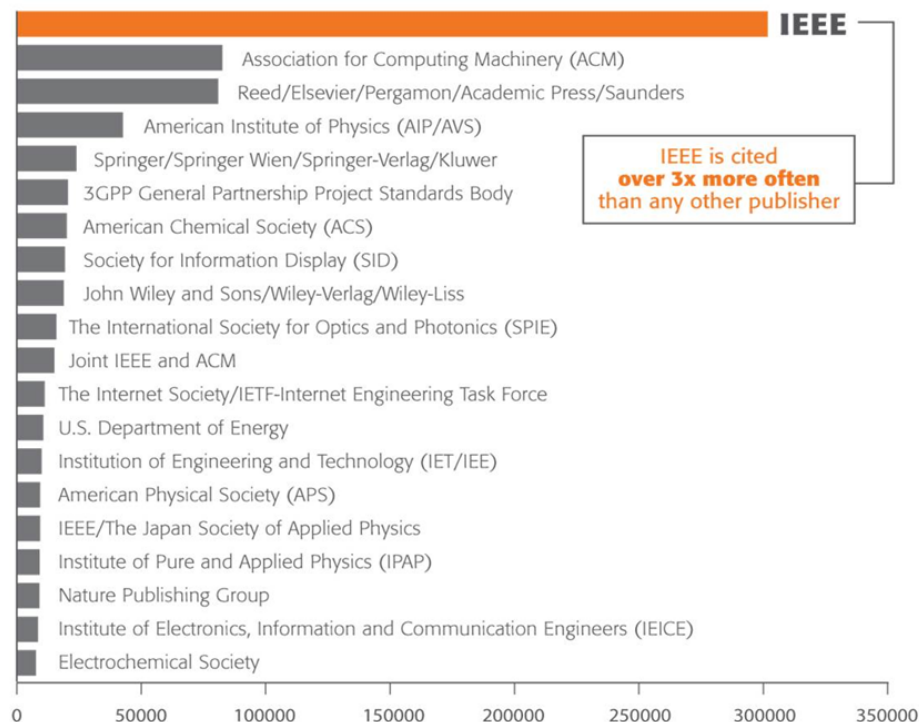
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Systems, Man, and Cybernetics: Systems, IEEE Transactions on

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2. M. Ghovanloo and K. Najafi "A modular 32-site wireless neural stimulation microsystem", *IEEE J. Solid-State Circuits*, vol. 39, no. 12, pp.2457 -2466 2004
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3. K. Chen , Z. Yang , L. Hoang , J. Weiland , M. Humayun and W. Liu "An integrated 256-channel epiretinal prosthesis", *IEEE J. Solid-State Circuits*, vol. 45, no. 9, pp.1946 -1956 2010
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Waveform Relaxation Time Domain Solver for Subsystem Arrays

Ciulio Antonini, Senior Member, IEEE, and Albert E. Ruehli, Life Fellow, IEEE

Abstract—In this paper we present a waveform relaxation approach for the transient analysis of 3-D electromagnetic problems using the partial element equivalent circuit (PEEC) method. Relying on a weaker coupling among separated systems, a waveform relaxation scheme is proposed to accelerate the transient analysis of large electromagnetic problems. The result is a comparison with those obtained using a conventional PEEC formulation. They exhibit a significant speed-up while preserving the solution accuracy.

Index Terms—Partial element equivalent circuit (PEEC) method, partitioning, of circuits, transient analysis, waveform relaxation technique.

I. INTRODUCTION

TODAY, a large number of classical techniques and solvers are available for the solution of conventional electromagnetic problems. This has greatly increased the ability to solve relevant design issues for integrated circuit, package and antenna problems. However, remaining challenges in this area are represented by the solution of larger problems in both the time and frequency domain. New techniques exist today for large problems based on integral equations. Most well-known approaches use iterative matrix solution-based [1], [2], multipole techniques [3], [4], and singular value decomposition (SVD) and QR-based reduction methods [5], [6]. There is also a growing need in industry for the solution of large, complex combined circuit and electromagnetic problems.

Issues which have limited the solution of such large problems in the past are due to memory size limitations and the performance of single processor machines. Fortunately, lower cost multi-processor architectures are now available for parallel processing

algorithms must be found which are suited for the solution of large problems using parallel processors. Such solutions clearly are based on subdividing a problem into subproblems so that multiple processors can be applied.

The partial element equivalent circuit (PEEC) technique, which provides 3-D electromagnetic models in the circuit domain, has evolved over the years. Much progress has been made in the capabilities of electromagnetic PEEC modeling from its origin [7] to numerous extensions including volume and surface-based techniques and non-orthogonal models [8]. It is a natural approach for the solution of combined circuits and electromagnetic problems in the circuit domain.

A choice of the most efficient solution techniques is of importance for large problems since algorithms often are tailored to a special situation. In this paper we address a special subclass of problems that consist of a potentially very large number of subsystems which do not exhibit very strong direct connections. The approach we are applying is of the subclass of domain composition methods called waveform relaxation (WR) which was first developed in [9]. Further, a more detailed description of WR techniques is presented in [10]. Today, new application areas are considered for the WR techniques besides the class of circuit problems which were solved in the early days of the approach [9]. WR has also been applied to 2-D transmission line problems, e.g., [11]–[14]. Recently, the WR technique has been successfully used to model multiple coupled transmission lines by exploiting the partitioning due to weak transverse couplings [15], [16]. Earlier, WR was also applied to analyze a simple antenna model in the time domain [17].

Here, we consider the application of WR for electromagnetic problems in the time domain. The approach is directly related

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This family of standards is defined with the intention that the criteria are technically feasible to achieve, but that only products demonstrating the leading environmental performance currently available in the marketplace would meet them at the time of their adoption. As the environmental performance of products that are available in the marketplace improves, it is intended that the criteria will be updated and revised to set a higher performance standard for leadership products.

This standard is intended to serve as a baseline for further environmental standards for additional electronic products to be developed in the future. References to IEEE Std 1680 likewise reference, unless otherwise specified, the individual product standards in the IEEE 1680 family of standards.

1.3 Application

The environmental performance criteria are contained in the standards that are members of this IEEE 1680 family of standards. The principles and procedures identified in Clause 1 apply to ~~notebook-personal computers, desktop-personal computers, and personal-computer monitors. The principles and procedures identified in Clause 1, Clause 2, and Clause 3 apply to personal-computer~~ electronic products and will apply to future standards developed for additional electronic products.

Different configurations of a product, as defined in the standards in this family, may include options for processors, memory, hard disks, etc. A product, for the purpose of this family of standards, is every configuration that could be offered in a specific marketing model and chassis type. If ~~there is a specific configuration within a marketing model and chassis type that would change~~ configurations do not meet the environmental performance substantially, especially if that configuration would no longer meet a criterion criteria as declared, then the manufacturer ~~could not claim conformance to this Standard for that configuration, even if the same model in other configurations did conform to this Standard. The manufacturer shall clearly report such special to the Product Registration Entity which~~ configurations ~~that do not conform to meet the Standard to the Product Registration Entity~~ criteria as declared.

A product includes ~~a desktop computer, a notebook computer or monitor,~~ an electronic product and all the peripherals that are integral to its operation. For example, the desktop computer together with the keyboard, the mouse, and the power cord would be a product.

1.4 Conformance with the IEEE 1680 family of standards

A manufacturer may designate one or more specific geographic regions, or countries, for which a product declaration is applicable. In order to conform to this Standard be conformant in a designated region or country, each unit of a the product that is sold in that region or country must satisfy all of the applicable

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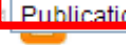
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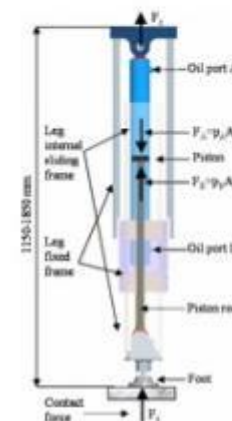


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For aircraft navigation, fault detection and isolation is paramount. Faults that may cause HMI must be noticed and mitigated in real-time. The operation of GPS has been very reliable and clearly reflects the extraordinary skill of the Air Force personnel that operate and maintain GPS. However, faults have occurred. Some are man-made and others are due to Mother Nature.

For example, the navigation data broadcast by the GPS satellites occasionally contain significant errors. As shown in Fig. 3, the GPS satellites are monitored by a relatively sparse ground control network (five stations are shown in Fig. 3 but eight new stations have been recently added [5]). Measurements at the ground stations are used to predict the orbit of the GPS satellites. These predictions are uploaded to the satellite and broadcast to the users. Generally, this estimated orbit is within 1 or 2 m of the true orbit [6]. However, the broadcast

6. Broadcast vs. precise GPS ephemerides: A historical perspective

D. L. M. Warren, J. F. Raquet

GPS Solutions, Vol. 7, issue (3), 2003-12

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IEEE Congratulates 2014 Nobel Prize Winners in Physics

The 2014 physics award winners are Isamu Akasaki of Meijo University and Nagoya University in Japan, Hiroshi Amano of Nagoya University in Japan, and Shuji Nakamura of the University of California, Santa Barbara in the USA. All three Nobel Prize winners are IEEE authors, published in IEEE Xplore:

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OR

NOT

NEAR

ONEAR

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SEARCH GUIDELINES

Operators need to be in all caps – i.e. AND/OR/NOT/NEAR.

Asterisk wildcards cannot be used within quotes or with the NEAR/ONEAR operators.

There is a maximum of 15 search terms.

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Data Fields

Operators

computer NEAR/3 "embeded system"

SEARCH GUIDELINES

Operators need to be in all caps – i.e. AND/OR/NOT/NEAR.

Asterisk wildcards cannot be used within quotes or with the NEAR/ONEAR operators.

There is a maximum of 15 search terms.

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解釋:

A NEAR/3 B:檢索結果A和B的辭語距離於3個字內(A可出現於B之前或後)

A ONEAR/3 B:檢索結果A和B的辭語距離於3個字內,且A只出現於B之前

使用雙引號 " " 會將此搜索單字或辭語限定為固定搜索結果

Tips 檢索技巧 說明

Search #1: **database AND query NEAR/3 optimiz*** 無法顯示搜尋結果

- Wildcard(*)無法與proximity searches (NEAR/ONEAR)一起使用
- 只有在命令檢索中才能使 proximity searches
- 以下為命令檢索建議搜尋方式：

database AND ((query NEAR/3 optimize) OR (query NEAR/3 optimization))

Note: optimize will pick up optimize, optimizes, optimized, and optimizing. It will also pick up British spelling variations (optimize picks up optimise, optimises, etc.)

Search #2: **database AND "query optimi"**無法顯示搜尋結果

- Wildcard(*)無法與雙引號一起使用
- 結合雙引號和OR運算符號來搜尋所有可能的結果

"query optimize" OR "query optimizing" OR "query optimization"


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☐ Gupta, R. (?)

SEARCH RESULTS


You searched for: **computer NEAR/3 "embedded system"**

Results per Page Showing 1 - 25 of 42 results [Next>](#)

Sort By: [Select All on Page](#) | [Deselect All](#)

☐ **[Design and Implementation of Embedded SNMP Network Management Manager in Web-Based Mode](#)** 
Xinyi Wu; Yong Zhu; Xiaolong Deng;
[Asia-Pacific Services Computing Conference, 2008. APSCC '08. IEEE](#)
Digital Object Identifier: [10.1109/APSCC.2008.151](#)
Publication Year: 2008 , Page(s): 1512 - 1516
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With the rapid development of **computer technology**, the **embedded system** has widely applied in many industrial fields. And these applications made it possible that the traditional personal-computer central pattern of computer transferred into the embedded-device central pattern. Nowadays, embedding Web-based SNMP manager into embedded devices is a hotspot of network management study with great significance. In this paper, we present the architecture of embedded network management based on SNMP agent and Web mode in Linux. It will enforce the abundant functions of network management in SNMP mode and expand the application scope of embedded-device in limited resource. Then we also draw a conclusion of critical research technology and developing policy

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*Required fields

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[Text Box]

*Last/Family/Surname:
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Enter e-mail address & password
The e-mail address provided here will be the username of your account

*E-mail address:
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*Confirm Password:
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What is a valid password?
Password Strength

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*Security question 2:
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☐ Integration and Packaging MEMS Directly Above Active CMOS

Pleters, P.; Qi, D.; Witvrouw, A.
High Density packaging and Microsystem Integration, 2007, HDP '07, International Symposium on Digital Object Identifier: 10.1109/HDP.2007.4283554
Publication Year: 2007, Page(s): 1
IEEE CONFERENCE PUBLICATIONS

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☐ Challenges in the packaging of MEMS

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IEEE Congratulates Nobel Prize Winners in Physics

The 2014 physics award winners are Isamu Akasaki of Meijo University and Nagoya University in Japan, Hiroshi Amano of Nagoya University in Japan, and Shuji Nakamura of the University of California, Santa Barbara in the USA. All three Nobel Prize winners are IEEE authors, published in IEEE Xplore:

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